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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/484,432	01/18/2000	Muneki Ando	35.C14218	9693

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EXAMINER

ABDULSELAM, ABBAS L

ART UNIT	PAPER NUMBER
2674	

DATE MAILED: 01/23/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No. 09/484,432	Applicant(s) Muneki And Osamu
Examiner Abbas Abdulselam	Group Art Unit 2674



Responsive to communication(s) filed on _____.

This action is FINAL.

Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11; 453 O.G. 213.

A shortened statutory period for response to this action is set to expire 3 month(s), or thirty days, whichever is longer, from the mailing date of this communication. Failure to respond within the period for response will cause the application to become abandoned. (35 U.S.C. § 133). Extensions of time may be obtained under the provisions of 37 CFR 1.136(a).

Disposition of Claims

Claim(s) 1-51 is/are pending in the application.

Of the above, claim(s) _____ is/are withdrawn from consideration.

Claim(s) _____ is/are allowed.

Claim(s) 1-51 is/are rejected.

Claim(s) _____ is/are objected to.

Claims _____ are subject to restriction or election requirement.

Application Papers

See the attached Notice of Draftsperson's Patent Drawing Review, PTO-948.

The drawing(s) filed on _____ is/are objected to by the Examiner.

The proposed drawing correction, filed on _____ is approved disapproved.

The specification is objected to by the Examiner.

The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. § 119

Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d).

All Some* None of the CERTIFIED copies of the priority documents have been

received.

received in Application No. (Series Code/Serial Number) _____.

received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

*Certified copies not received: _____.

Acknowledgement is made of a claim for domestic priority under 35 U.S.C. § 119(e).

Attachment(s)

Notice of References Cited, PTO-892

Information Disclosure Statement(s), PTO-1449, Paper No(s). _____

Interview Summary, PTO-413

Notice of Draftsperson's Patent Drawing Review, PTO-948

Notice of Informal Patent Application, PTO-152

--- SEE OFFICE ACTION ON THE FOLLOWING PAGES ---

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DETAILED ACTION

1. A certified copy of foreign document has been received.

Claim Rejections U.S.C. 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371© of this title before the invention thereof by the applicant for patent.

Claims 1-51 are rejected under 35 U.S.C. 102(e) as being anticipated by Gyouten et al.

(USPN 6195077).

Regarding claims 1, 8, 15, and 49-51, Gyouten teaches a liquid crystal panel (101) with segment drive circuit (102), and side drive circuit (103) which is used for selecting sequentially to drive scanning lines. Gyouten teaches displaying images in a simple matrix type which displays an image with a pixel located at each intersections of the electrodes (X1, Y1), (X2, Y2), (X3, Y3).....(Xm, Yn). See column 11, lines 47-58 and Fig 1. Gyouten teaches an output control means for adjusting an amount of correction for the output voltage of the segment side circuit according to the distance between an arrangement position of the segment drive circuit and a position of scanning line selected by the side drive circuit in the liquid crystal panel. See column 1, lines 11-14, column 4, lines 42-47, and Fig 39. Moreover, Gyouten teaches correction clock

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generator circuit (70) in conjunction with the correction base clock for indicating the position where a correction period is to be provided, and the length of correction period is adjusted by the correction clock generator circuit. In addition, Gyouten teaches counter (72) changes in the outputs (B1, B2, B3) to high level ; and further teaches the display data stored in the line latch (123) of the drive circuit (102) that would be given to the liquid crystal drive output circuit (126). See column 1, lines 55-63 and Fig 41. Gyouten also teaches maintaining uniformity of luminance as well as the voltage waveforms with the correction voltage changes. See column 17, lines 30-33 , lines 49-65 and Fig 20. Therefore an identical image display system is shown as taught by Gyouten.

Regarding claims 2-3 and 9-10, Gyouten teaches the pulse width modulator (203), and correction clock with modulator (204) which is supplied with reference correction clock signals. See Fig 29.

Regarding claims 4, 6, 11 and 13, Gyouten teaches the correspondence of counters (71, 72) along with the rise and fall of correction clock signals. See column 16, lines 58-61.

Regarding claims 5, 7, 12, and 14, Gyouten teaches changing of the length of correction period. See column 16, lines 5-11 and Fig 14.

Regarding claims 16, Gyouten teaches the segment output voltage Vs. which is selected according to a combination of the AC-converting signal, the line latch output, and the correction clock. See column 20, lines 26-35 and Fig 28.

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Regarding claims 17-18, 21-22, 28-29, 35-36, and 42-43, Gyouten teaches liquid crystal panel (1), and drive circuit (2) which drives the plurality of segment electrodes both in a row and column. See column 11, lines 47-57 and Fig 1.

Regarding claims 19, 23, 30, 37 and 44, Gyouten teaches a method for driving the LCD device with a common drive circuit for selecting sequentially to drive scanning lines.

Regarding claims 20, 24-27, 31-34, 38-41 and 44-48, Gyouten teaches the liquid crystal panel (101) with common electrodes, segment electrodes and liquid crystal layer interposed between electrodes. Column 2, lines 9-12. In addition, it is well known in the art and would be obvious to utilize a display panel composed of electron emission devices with a phosphor layer.

Conclusion

3. The prior art made of record and not relied upon is considered to applicant's disclosure.

The following arts are cited for further reference.

U.S. Pat No. 5,521,611 to Okada et al.

U.S. Pat No. 5,841,411 to Francis

U.S. Pat No. 5,646,643 to Hirai et al.

U.S. Pat No. 5,969,713 to Tomizawa et al.

U.S. Pat No. 6,115,018 to Okumura et al.

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4. Any inquiry concerning this communication or earlier communication from the examiner should be directed to **Abbas Abdulselam** whose telephone number is **(703) 305-8591**. The examiner can normally be reached on Monday through Friday (9:00-5:30).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, **Richard Hjerpe**, can be reached at **(703) 305-4709**.

Any response to this action should be mailed to:

Commissioner of patents and Trademarks
Washington, D.C. 20231

or faxed to:

(703) 872-9314

Hand delivered responses should be brought to crustal park II, Crystal Drive, Arlington, VA, Sixth Floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology center 2600 customer Service office whose telephone number is **(703) 306-0377**.



RICHARD HJERPE
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600

Abbas Abdulselam

Examiner

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